IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Lee et al.**	§	
	§	Group Art Unit: **2133**
Serial No.: **09/965,000**	§	
	§	Examiner: **Unknown**
Filed: **September 27, 2001**	§	
	§	Attorney Docket No.: **AUS920010672US1**
For: **Method and Apparatus for Verifying Hardware Implementation of a Processor Architecture in a Logically Partitioned Data Processing System**	§ § § § § §	

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.97

Hon. Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

Applicants request that the information listed on the attached Form PTO-1449 be considered by the Office during the pendency of the above entitled application, pursuant to 37 C.F.R. 1.97.

Please charge any fees necessary for prosecution of the present application to Deposit Account No. 09-0447. If any extension of time is required, such extension is hereby requested. Please charge any additional required fee for extension of time to Deposit Account No. 09-0447.
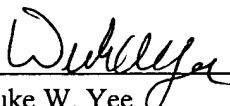
In accordance with 37 C.F.R. 1.97(h), the filing of this Information Disclosure Statement shall not constitute an admission that any information cited therein is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56(b). In the interest of full and complete disclosure to the Office, some or all of the art cited herein may not be considered by Applicant(s) or the Undersigned to be material under the new standards of materiality defined in 37 C.F.R. 1.56(b), enacted March 16, 1992, but may be material under the old standard of materiality defined in 37 C.F.R. 1.56(a), last amended on November 28, 1988, or may merely be technical background which

may be of interest to the Examiner. In accordance with 37 C.F.R. 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made.

This Information Disclosure Statement is being filed under 37 C.F.R. § 1.97(b) within three months of the filing date of the application, or before the mailing date of a first office action on the merits. No fee is required.

Respectfully submitted,

Date: ___1/127/6(___

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U.S. DEPT. OF COMMERCE PATENT AND TRADEMARK OFFICE

Form PTO-1449 **LIST OF PRIOR ART CITED BY APPLICANT** (Use several sheets if necessary)	ATTORNEY DOCKET NO. **AUS920010672US1**	SERIAL NO. **09/965,000**
	APPLICANT **Lee et al.**	
	FILING DATE **September 27, 2001**	GROUP ART UNIT **2133**

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
	AA	4564903	Jan. 14, 1986	Guyette et al.	364/300	Oct. 5, 1983
	AB	4843541	Jun. 27, 1989	Bean et al.	364/200	Jul. 29, 1987
	AC	5345590	Sep. 6, 1994	Ault et al.	395/650	Sep. 1, 1993

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO

OTHER PRIOR ART (including author, title, date, pertinent page, etc.)

	AD	IBM Technical Disclosure Bulletin, "Hypervisor High Performance Synchronous Dispatch for Coupled Systems", Vol. 39, No. 12, December 1996
	AE	IBM Technical Disclosure Bulletin, "VM MPG Operating as a DRF Hypervisor as a First Level Guest Under PR/SM", Vol. 38, No. 04, April 1995, p. 325
	AF	IBM Technical Disclosure Bulletin, "Sharing Read-Only Memory among Multiple Logical Partitions", Vol. 36, No. 03, March 1993, pp. 303-304
	AG	IBM Technical Disclosure Bulletin, "Highly Parallel Coupling Facility Emulator/Router with Shadowed Link Buffers", Vol. 39, No. 12, December 1996
	AH	IBM Technical Disclosure Bulletin, "Coordinating Multiple Server Partitions to Enter Power-Save State", Vol. 39, No. 06, June 1996, pp. 235-239.

DATE CONSIDERED	EXAMINER

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

0001RF-108/49776.1